



2819
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XA-9472
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasuhisa SHIMAZAKI et al.

Appln. No.: 09/855,660

Group Art Unit: 2819

Filed: May 16, 2001

Examiner: D. Chang

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING HIGH-SPEED
AND LOW-POWER LOGIC GATES WITH COMMON TRANSISTOR
SUBSTRATE POTENTIALS, AND DESIGN DATA RECORDING
MEDIUM THEREOF

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

It is respectfully requested that the time now set for
response be extended, retroactively, by three months. The
required petition fee for a large entity is submitted
herewith. The Commissioner is hereby authorized to charge any
fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by
this paper to Deposit Account No. 50-1165.

12/19/2003 HALI11 00000050 09855660

01 FC:1253

950.00 DP

Respectfully submitted,

MWS:sjk

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By:


Mitchell W. Shapiro

Reg. No. 31,568

December 18, 2003

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